

FORM PTO-892		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>09/105,528</b>	GROUP ART UNIT <b>2171</b>	ATTACHMENT TO PAPER NO.	<b>22</b>
<b>NOTICE OF REFERENCES CITED</b>				APPLICANT(S)  <b>Knowles</b>			
<b>U.S. PATENT DOCUMENTS</b>							
*		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	A						
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<b>FOREIGN PATENT DOCUMENTS</b>							
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB- CLASS
	L						
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	N						
	O						
	P						
	Q						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	R	Gedye et al "Browsing the chip design database", IEEE 1988, pages 269-274.					
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EXAMINER <b>Uyen Le</b>			DATE <b>June 15, 2003</b>				
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* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05(a).)							